

FORM PTO - 1449 INFORMATION DISCLOSURE STATEMENT		ATTORNEY DOCKET NO.: ASC-012DV APPLICANTS: Lee et al. SERIAL NO.: Not yet assigned 10/788741 FILING DATE: Herewith 2-27-04 GROUP: Not yet assigned					
---	--	---	--	--	--	--	--

## U.S. PATENT DOCUMENTS

EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TN	A22	6,096,590	08/01/2000	Chan et al.			
TN	A23	6,107,653	08/22/2000	Fitzgerald			
TN	A24	6,111,267	08/29/2000	Fischer et al.			
TN	A25	6,117,730	09/12/2000	Bensabat et al.			
TN	A26	6,130,453	10/10/2000	Mei et al.			
TN	A27	6,143,636	11/07/2000	Forbes et al.			
TN	A28	6,204,529	03/20/2001	Lung et al.			
TN	A29	6,207,977 B1	03/27/2001	Augusto			
TN	A30	US 2001/0003364 A1	06/14/2001	Sugawara et al.			
TN	A31	6,249,022	06/19/2001	Lin et al.			
TN	A32	6,251,755 B1	06/26/2001	Furukawa et al.			
TN	A33	6,266,278	07/24/2001	Harari et al.			
TN	A34	US 2002/0100942 A1	08/01/2001	Fitzgerald et al. 2002			
TN	A35	6,339,232 B1	01/15/2002	Takagi			

## FOREIGN PATENT DOCUMENTS

EXAM. INIT.		DOCUMENT NUMBER	DATE	COUNTRY CODE	CLASS	SUB CLASS	FILING DATE	ABSTRACT ONLY	ENGLISH LANG (Y/N)
TN	B17	WO 02/15244A2	02/21/2002	PCT				No	Yes
TN	B18	WO 02/13262 A2	02/14/2002	PCT				No	Yes
TN	B19	WO 02/47168 A2	06/13/2002	PCT				No	Yes
TN	B20	WO 02/071488 A1	09/12/2002	PCT				No	Yes
TN	B21	WO 02/071491 A1	09/12/2002	PCT				No	Yes

## OTHER ART, JOURNAL ARTICLES, ETC.

EXAM. INIT.	OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)						
TN	C18	König et al., "Design Rules for n-Type SiGe Hetero FETs," <u>Solid State Electronics</u> , Vol. 41, No. 10 (1997), pp. 1541-1547.					
TN	C19	Höck et al., "Carrier mobilities in modulation doped Si <sub>1-x</sub> Ge <sub>x</sub> heterostructures with respect to FET applications," <u>Thin Solid Films</u> , Vol. 336 (1998) pp. 141-144.					

EXAMINER T. Nguyen DATE CONSIDERED 6/22/06

MR  
9-56

FORM PTO - 1449 INFORMATION DISCLOSURE STATEMENT		ATTORNEY DOCKET NO.: ASC-012DV APPLICANTS: Lee et al. SERIAL NO.: Not yet assigned FILING DATE: Herewith 10/28/04 2/27/04 GROUP: Not yet assigned
---	--	--

## U.S. PATENT DOCUMENTS

EXAM. INIT.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TV	A36 6,350,993 B1	02/26/2002	Chu et al.			
TV	A37 US 2002/0125471 A1	09/12/2002	Fitzgerald et al.			12/04/2001
TV	A38 US 2002/140631-A1	10/03/2002	Rim 2002/0140631			03/31/2001
TV	A39 US 2002/0125497 A1	09/12/2002	Fitzgerald			07/16/2001

## FOREIGN PATENT DOCUMENTS

EXAM. INIT.	DOCUMENT NUMBER	DATE	COUNTRY CODE	CLASS	SUB CLASS	FILING DATE	ABSTRACT ONLY	ENGLISH LANG (Y/N)
TV	B22 WO 02/071495 A1	09/12/2002	PCT				No	Yes

## OTHER ART, JOURNAL ARTICLES, ETC.

EXAM. INIT.	OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)	
TV	C20	Maiti et al., "Strained-Si heterostructure field effect transistors," <i>Semicond. Sci. Technol.</i> , Vol. 13 (1998) pp. 1225-1246.
TV	C21	Hackbart et al., "Strain relieved SiGe buffers for Si-based heterostructure field-effect transistors," <i>Journal of Crystal Growth</i> , Vol. 201 (1999) pp. 734-738
TV	C22	Armstrong, "Technology for SiGe Heterostructure-Based CMOS Devices," Submitted to the Massachusetts Institute of Technology Department of Electrical Engineering and Computer Science on June 30, 1999, pp. 1-154.
TV	C23	O'Neill et al., "SiGe Virtual substrate N-channel heterojunction MOSFETs," <i>Semicond. Sci. Technol.</i> , Vol. 14 (1999) pp. 784-789.
TV	C24	Rim, "Application of Silicon Based Heterostructures to Enhanced Mobility Metal-Oxide-Semiconductor Field-Effect Transistors," Ph.D. Thesis, Stanford University (July 1999) pp. 1-184
TV	C25	Parker et al., "SiGe heterostructure CMOS circuits and applications," <i>Solid State Electronics</i> , Vol. 43, No. 8, (August 1999) pp. 1497-1506.

EXAMINER	T. N. KUMAR	DATE CONSIDERED	6/22/06
----------	-------------	-----------------	---------

MR  
Op-5-6